

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Docket No.: TI-35731  
**Antonio F. Mondragon-Torres** Examiner: Lee, Siu M  
Serial No.: 10/699,707 Art Unit: 2611  
Filed: 11/03/2003 Conf. No.: 3525  
For: RECONFIGURABLE CHIP LEVEL EQUALIZER ARCHITECTURE

AMENDMENT - 37 CFR § 1.111

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

Responsive to the Office Action dated March 5, 2009, please amend the above-identified application as follows:

Amendments to the Claims begin on page 2 of this paper.

Remarks/Arguments begin on page 7 of this paper.